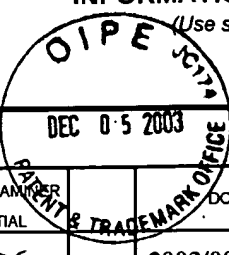


<b>INFORMATION DISCLOSURE CITATION</b> <i>(Use several sheets if necessary)</i>				ATTY DOCKET NO. <b>BU998050DIV1</b>		SERIAL NO. <b>09/805,200</b>	
				APPLICANT(S) <b>William P. Moore, et al.</b>			
				FILING DATE <b>March 14, 2001</b>		GROUP <b>2181</b>	



**U.S. PATENT DOCUMENTS**

*EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
SG	2003/0070013 A1	04/10/03	Hansson			
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**FOREIGN PATENT DOCUMENTS**


		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
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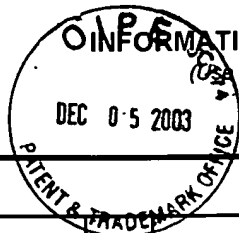
**OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)**

SG			Ching-Long Su, et al., "Low Power Architecture Design and Compilation Techniques for High-Performance Processors", 1994, Pages 489-498.

EXAMINER 	DATE CONSIDERED 4/6/04
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		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
							YES	NO

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SG			J. Patrick Brennan, et al. "Low Power Methodology and Design Techniques for Processor Design", 1998, Pages 268-273.

EXAMINER

Shane Derratt

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4/6/04

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